

12/10/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER.

<p>O.I.P.E.</p> <p>SCANNED <u>Cor</u> Q.A. <u> </u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/683351	CONT/PRIOR F	CLASS 712	SUBCLASS 210	ART UNIT 2183	EXAMINER <i>Chan, Eddie</i>
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209 227

Method for handling 32 bit results for an out-of-order processor
with A 64 bit architecture

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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